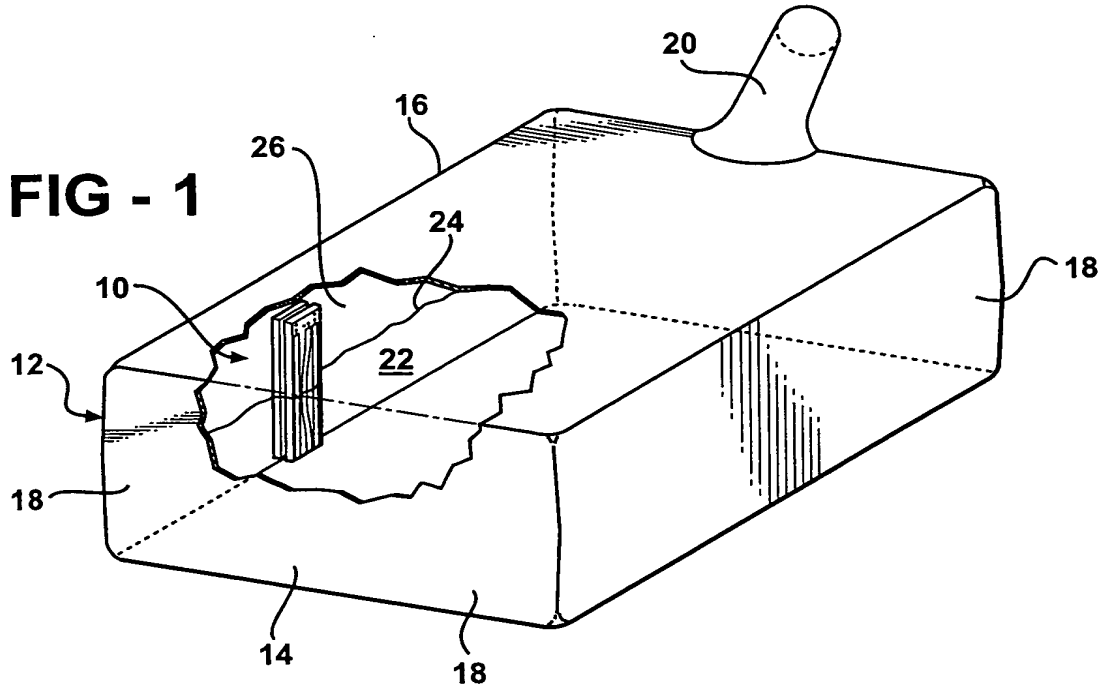
FIG - 1


20
16
26
24
10
22
12
18
18
14
18

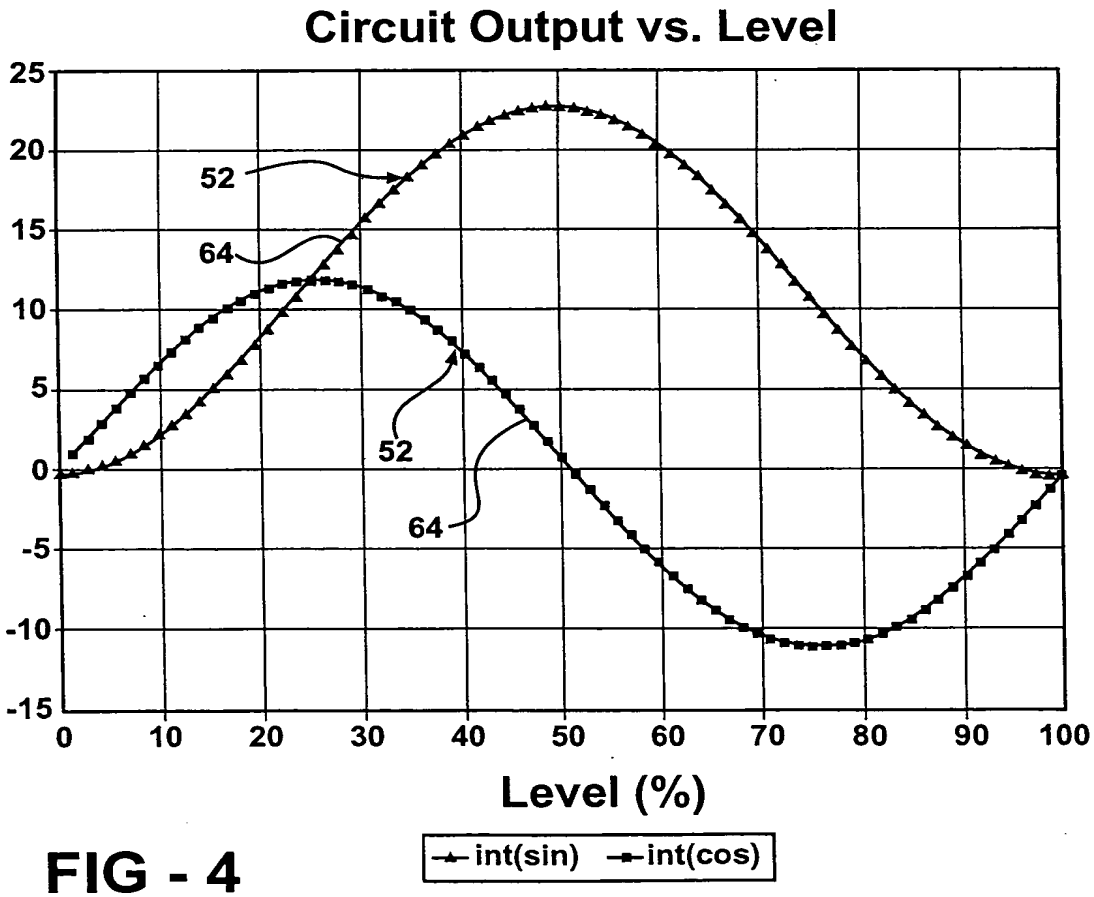

Circuit Output vs. Level
25
20
15
10
5
0
-5
-10
-15
0
10
20
30
40
50
60
70
80
90
100
Level (%)
52
64
52
64
int(sin)
int(cos)

FIG - 4

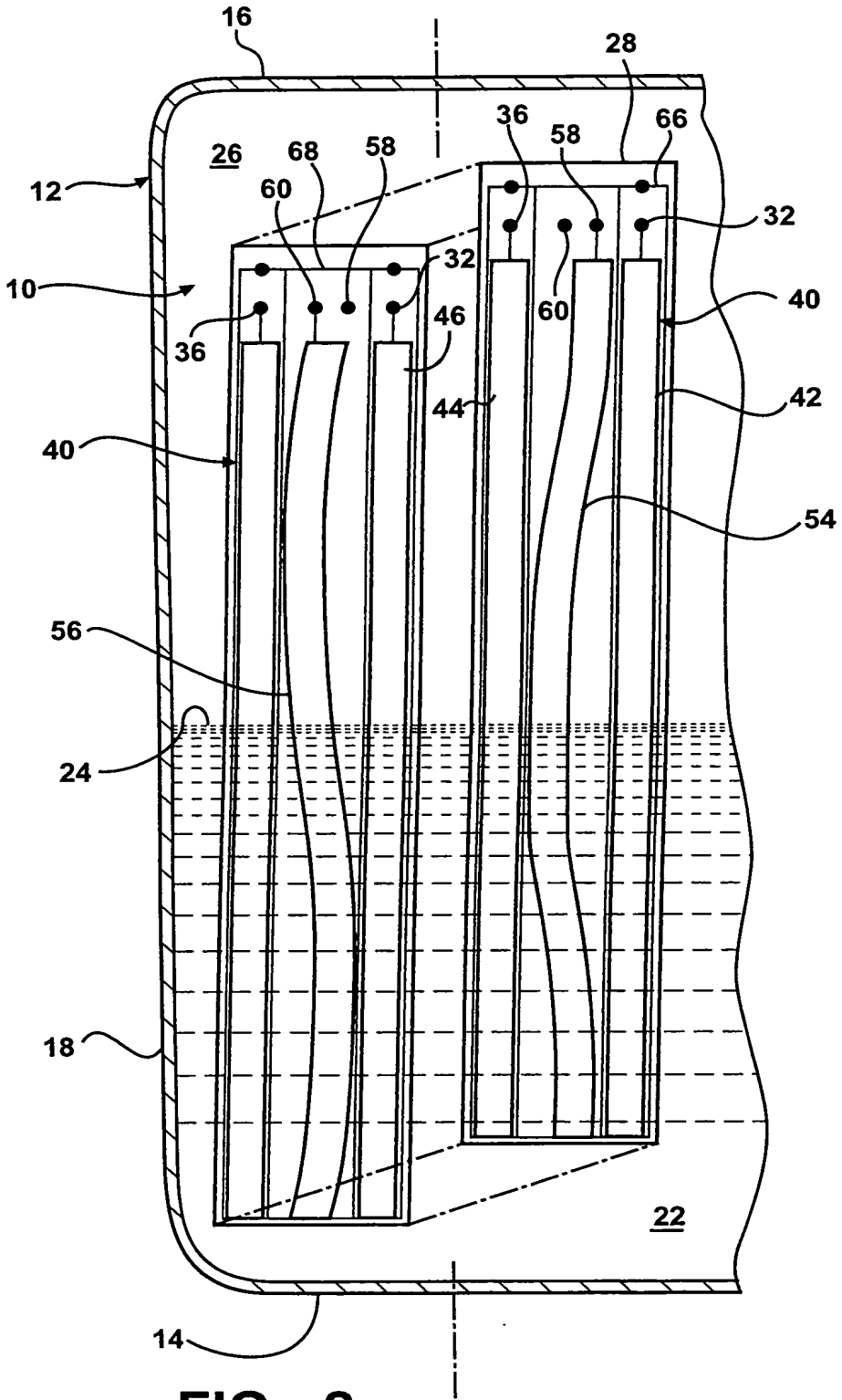

16
28
36
58
66
26
68
58
12
60
32
10
32
40
60
36
46
44
42
40
54
56
24
18
22
14

FIG - 2

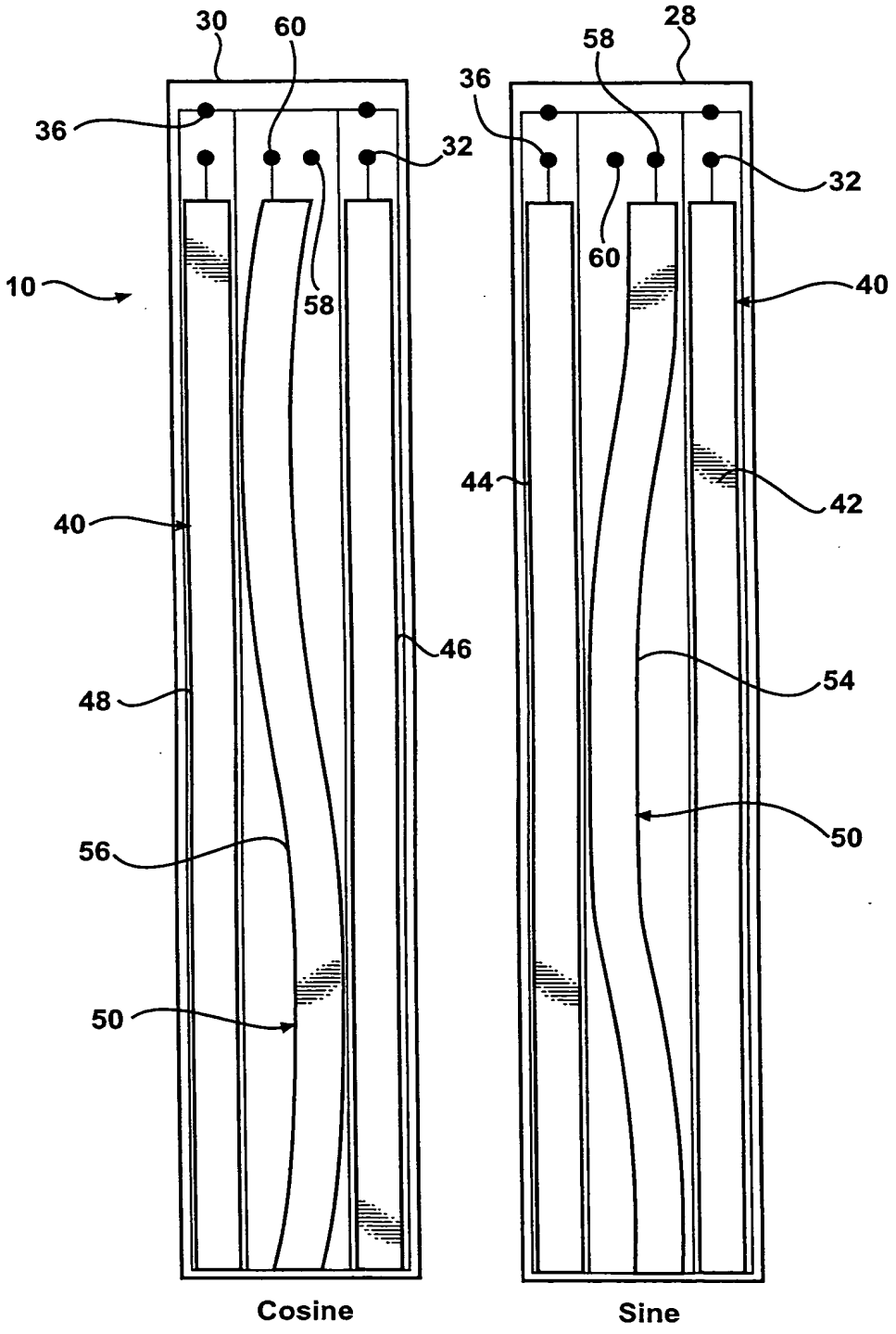

30
60
36
32
10
58
40
46
48
56
50
Cosine
28
58
36
32
60
40
44
42
54
50
Sine

FIG - 2A

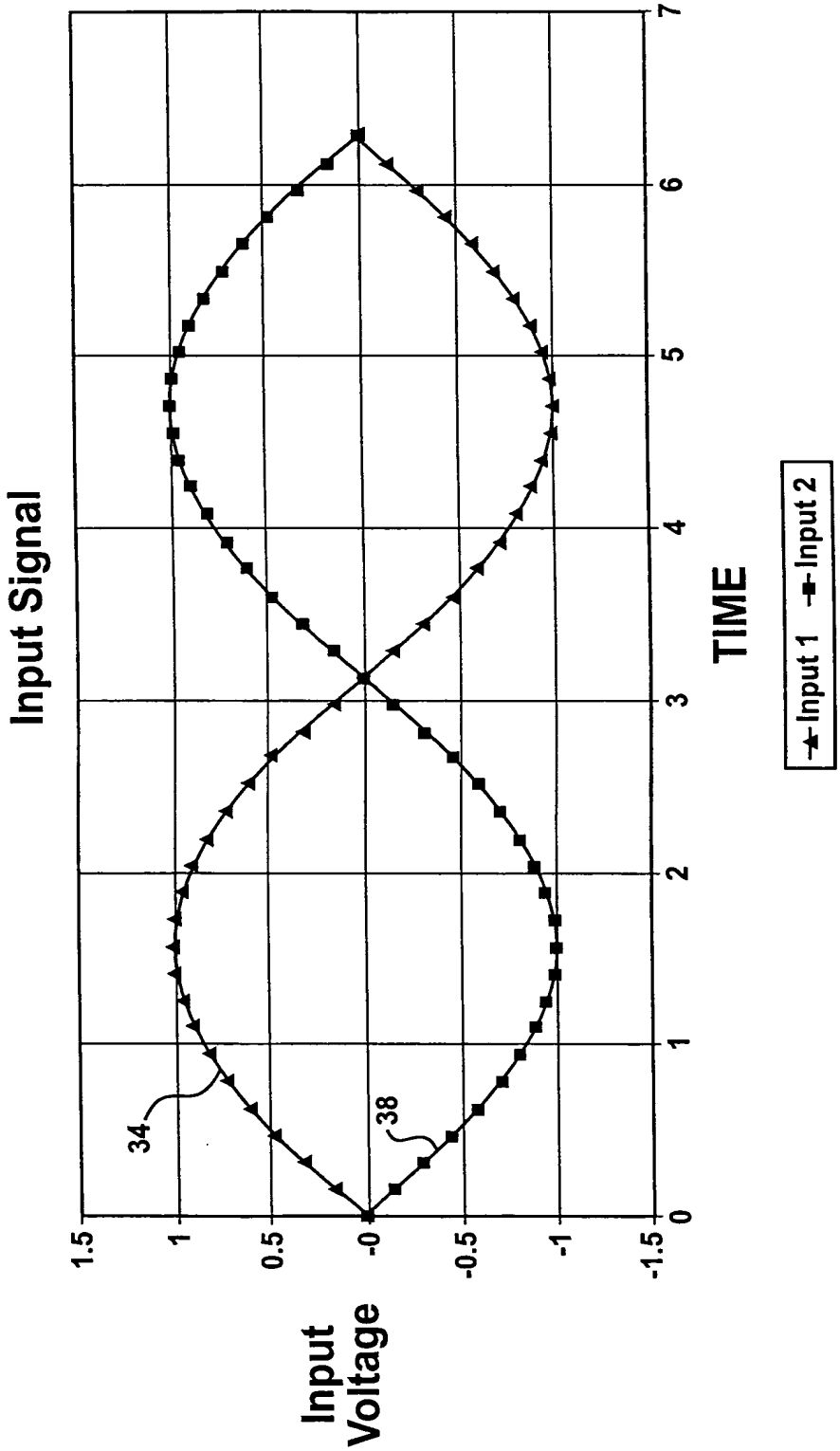

Input Signal
Input Voltage
TIME
1.5
1
0.5
-0
-0.5
-1
-1.5
0
1
2
3
4
5
6
7
34
38
Input 1
Input 2

FIG - 3

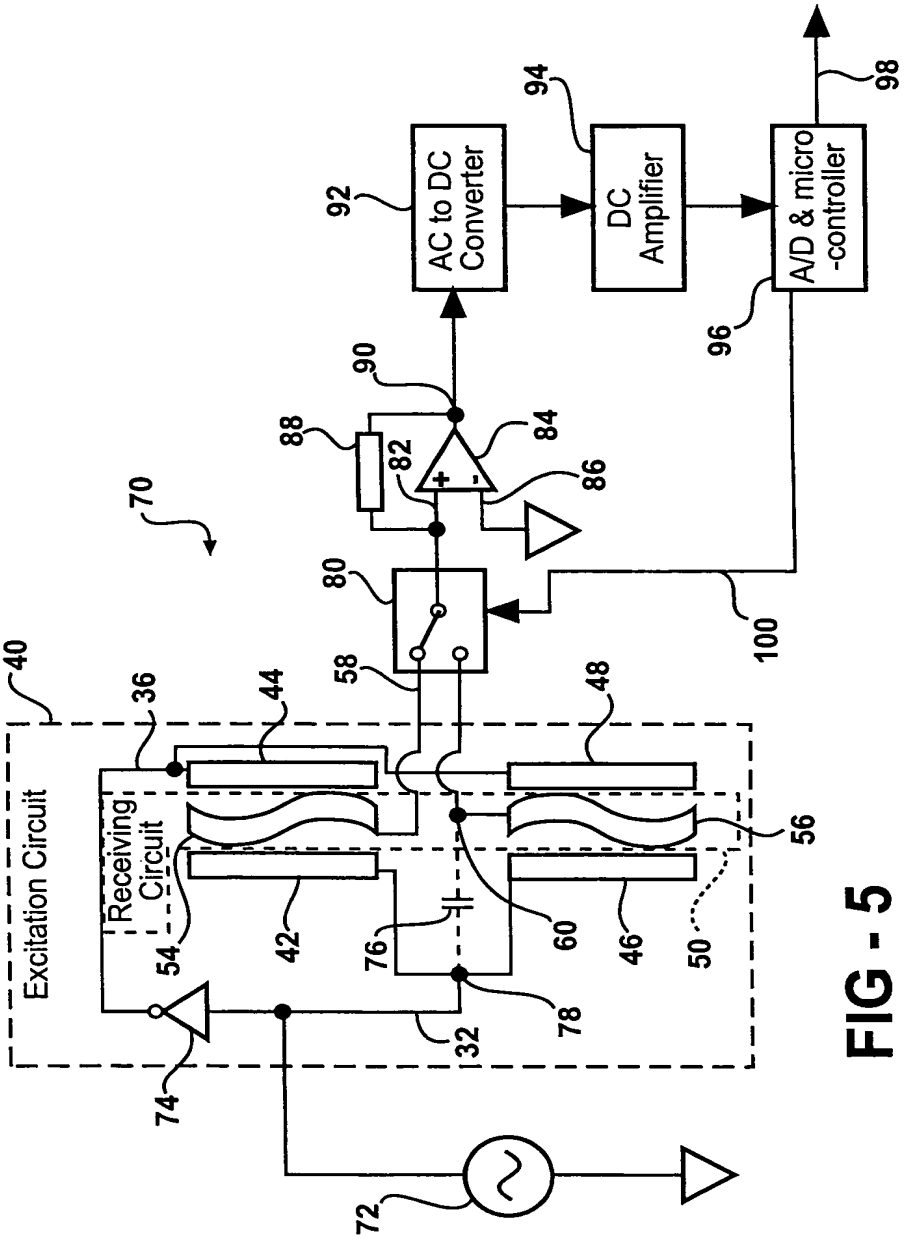

AC to DC Converter
DC Amplifier
A/D & micro -controller
Excitation Circuit
Receiving Circuit
+
-
92
94
98
96
90
88
82
84
86
70
80
100
58
40
36
44
48
56
54
42
76
60
46
50
78
32
74
72

FIG - 5